

ABSTRACT OF THE DISCLOSURE

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A control circuit allows a memory to start a data write through external access at a time point corresponding to timing O. When a reproduction switch is operated at a time point corresponding to timing Y, the control circuit interrupts the write operation at the memory by deactivating a write control signal. The control circuit also allows the memory to start a read operation by activating a read control signal. The data read out from the memory are provided to an encoder/decoder circuit. Once the data read is completed, the control circuit deactivates the read control signal and activates the write control signal at a time point corresponding to timing P. The memory resumes the data write operation through the external access which has been interrupted since the time point Y.